

Vishay Semiconductors

Insulated Ultrafast Rectifier Module, 120 A



SOT-227

PRODUCT SUMMARY				
V_R	400 V			
I _{F(AV)} at T _C = 65 °C	120 A			
t _{rr}	35 ns			

FEATURES

- Two fully independent diodes
- · Ceramic fully insulated package $(V_{ISOL} = 2500 V_{AC})$



- Ultrasoft reverse recovery current shape
- · Low forward voltage
- Optimized for power conversion: welding and industrial SMPS applications
- Industry standard outline
- Plug-in compatible with other SOT-227 packages
- · Easy to assemble
- · Direct mounting to heatsink
- UL approved file E78996



- Compliant to RoHS directive 2002/95/EC
- · Designed and qualified for industrial level

DESCRIPTION

The UFB120FA40P insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The planar structure of the diodes, and the platinum doping life time control, provide a ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, dc-to-dc converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Cathode to anode voltage	V _R		400	V
Continuous forward current per diode	I _F	T _C = 65 °C	60	۸
Single pulse forward current per diode	I _{FSM}	T _C = 25 °C	800	А
Maximum power dissipation per module	P _D	T _C = 90 °C	96	W
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 minute	2500	V
Operating junction and storage temperatures	T _J , T _{Stg}		- 55 to 150 °C	°C

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ELECTRICAL SPECIFICATIONS PER DIODE (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	$I_{R} = 100 \ \mu A$	400	-	-	
Forward voltage V _{FM}	I _F = 60 A	-	1.16	1.37	V	
	I _F = 60 A, T _J = 150 °C	-	0.96	1.13		
Reverse leakage current I _{RM}	l	$V_R = V_R$ rated	-	-	0.1	mΛ
	IRM	$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	-	1	- mA
Junction capacitance	C _T	V _R = 400 V	-	67	-	pF

DYNAMIC RECOVERY CHARACTERISTICS PER DIODE (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	30	35	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	65	-	ns
	T _J = 125 °C	$I_F = 50 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_B = 200 \text{ V}$	-	128	-		
Peak recovery current I _{RRM}	T _J = 25 °C		-	7.4	-	A	
	T _J = 125 °C		-	17.8	-		
Reverse recovery charge Q _{rr}		T _J = 25 °C	-n	-	240	-	nC
	T _J = 125 °C		-	1139	-	110	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single diode conducting	В		-	0.99	1.24	
Junction to case, both diodes conducting	- R _{thJC}		-	0.49	0.62	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque			-	1.3	-	Nm



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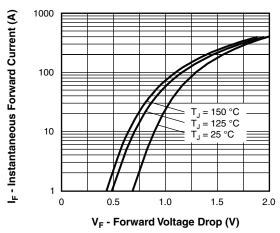


Fig. 1 - Typical Forward Voltage Drop Characteristics (Per Diode)

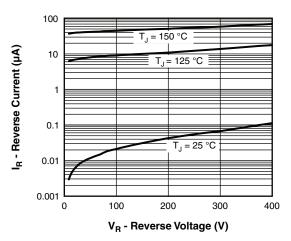


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

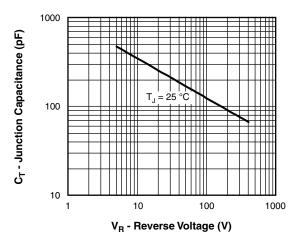


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

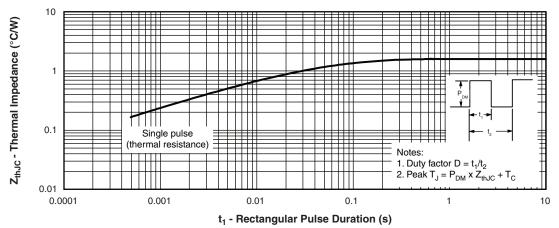


Fig. 4 - Maximum Thermal Impedance Z_{thJC} (Per Diode)

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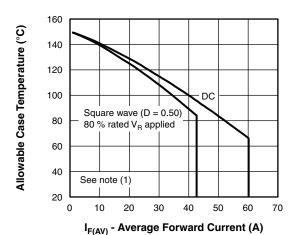


Fig. 5 - Maximum Allowable Case Temperature vs. Avarage Forward Current (Per Diode)

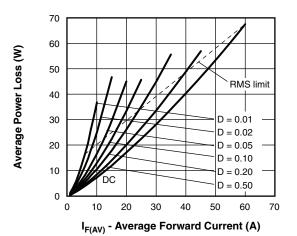


Fig. 6 - Forward Power Loss (Per Diode)

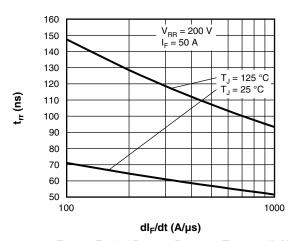


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

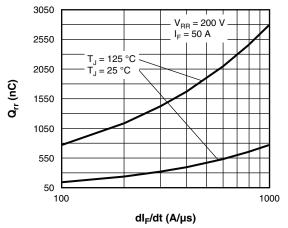


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$



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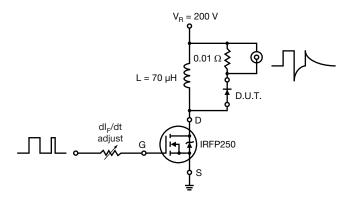
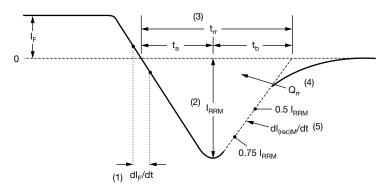


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) ${\rm I}_{\rm RRM}$ peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

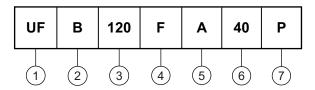
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ORDERING INFORMATION TABLE

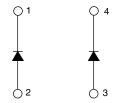
Device code



- Ultrafast rectifier
- 2 Ultrafast Pt diffused
- 3 Current rating (120 = 120 A)
- 4 Circuit configuration (2 separate diodes, parallel pin-out)
- 5 Package indicator (SOT-227 standard isolated base)
- 6 Voltage rating (40 = 400 V)
- 7 None = Standard production
 - P = Lead (Pb)-free

Quantity per tube is 10, M4 screw and washer included

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95036</u>					
Packaging information	www.vishay.com/doc?95037				



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